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	Filing Date		2000-06-04
	First Named Inventor	Derrick R. Meyer	
	Art Unit	2157 2457	
	Examiner Name	Burgess, Barbara N.	
	Attorney Docket Number	5500-96300	

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10	5502840	1996-03-26	Barton	
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5	Wolf-Dietrich Weber, et al. "Analysis of Cache Invalidation Patterns in Multiprocessors", Computer Systems Laboratory, Stanford University, CA, pp. 243-256.	<input type="checkbox"/>
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